

DOPANT REDISTRIBUTION AND ACTIVATION IN THIN FILM SOI/SIMOX SUBSTRATES

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The high quality of SOI/SIMOX substrates and the inherent good control and reproducibility of the process have established SIMOX as the front running SOI technology. The thin silicon films have good crystal perfection and generally form a planar, abrupt interface with the underlying dielectric. As a result high performance circuits, showing improved speed and radiation tolerance over bulk silicon structures, have been successfully fabricated in these substrates and the expectation is that following processing and device optimization further circuit improvements will be achieved. In the future it may be possible to use this material for other novel and unconventional device structures. However optimization calls for detailed structural and electrical characterization of the material, including evaluation of the transport properties of the thin silicon films.

In this paper we report experiments to determine the transport properties, electrical activity and redistribution of dopants implanted into SIMOX samples with different silicon layer thicknesses.

High temperature annealed SIMOX samples with silicon film thicknesses of 2000Å (SIMOX1) and 3000Å (SIMOX2) have been implanted with As⁺, Sb⁺, B⁺ and P⁺ ions. The ion energies were chosen so that the projected range was similar for each species, being ≈500Å. Doses were chosen to be similar to source/drain implantations used in CMOS devices. Activation of the dopant was achieved by annealing samples at either 950°C or 1150°C in flowing nitrogen gas in a resistivity heated furnace. The lower temperature was chosen to fully activate the dopant whilst the higher temperature allowed redistribution and segregation of the dopant to be studied. In addition, samples were isochronally (15 mins) annealed over the temperature range 200°C to 1150°C. Surface analysis techniques included 1.5MeV He⁺ Rutherford backscattering (RBS) and channelling, SIMS and cross-sectional TEM whilst electrical data was obtained by Four Point Probe and Van der Pauw techniques. Dopant profiles have been compared with SUPREM simulations.

Figure 1 typifies the results and shows As concentration depth profiles obtained from samples prepared from bulk silicon, SIMOX1 and SIMOX2, which have been implanted with a dose of 5×10^{15} As⁺ cm⁻² at 70keV and annealed at 950°C for 30 mins (curve (ii)) or 1150°C for 30 mins (curve (iii)). The profiles from the samples annealed at 950°C are the same, within experimental errors, confirming that similar diffusional broadening of the implanted profile (curve (i)) has occurred. Sheet resistance measurements show similar levels of dopant activation. In contrast, during the high temperature anneal, significant indiffusion of As occurs for bulk silicon whilst the SIMOX samples have essentially flat dopant profiles whose width is defined by the thickness of the silicon overlayer (marked by arrows in Figure 1(b),(c)). The uniform doping level and the absence of a concentration build up due to segregation at the Si/SiO₂ interface confirms

the high quality of the material. Values of the Hall mobility are 49 and 57 $\text{cm}^2\text{v}^{-1}\text{sec}^{-1}$ in SIMOX1 and SIMOX2, respectively, the differences being accounted for by the dependence upon carrier concentration.

Dopant activation during isochronal annealing is exemplified in Figure 2 which shows the temperature dependence of the sheet resistance following As^+ ion implantation into the same set of samples. The main difference is seen above 800°C when significant As diffusion occurs, which leads to uniform doping in the silicon layer and a value of sheet resistance which is temperature independent above 1000°C.

Depth profiles determined by RBS and SIMS will be presented for the other dopants B, P and Sb. The behaviour of all four dopants in SIMOX material will be evaluated and the consequent impact upon device geometries will be discussed.

References

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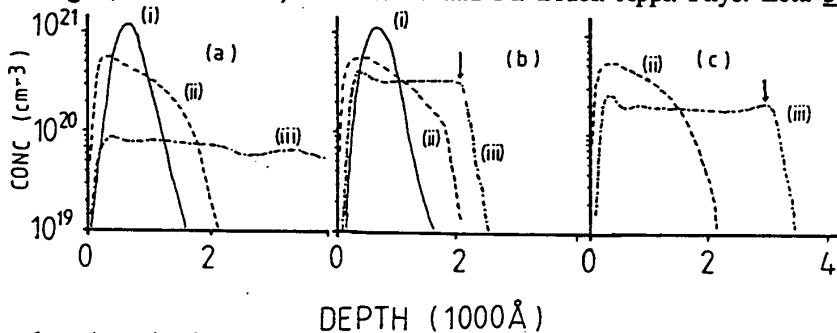


Figure 1 Arsenic depth profiles (i) as-implanted, (ii) 950°C 30 mins and (iii) 1150°C 2 hrs, in (a) bulk silicon, (b) SIMOX with 2000Å of silicon overlay and (c) SIMOX with 3000Å of silicon overlay. Samples implanted with $5 \cdot 10^{15} \text{As cm}^{-2}$ at 70keV.

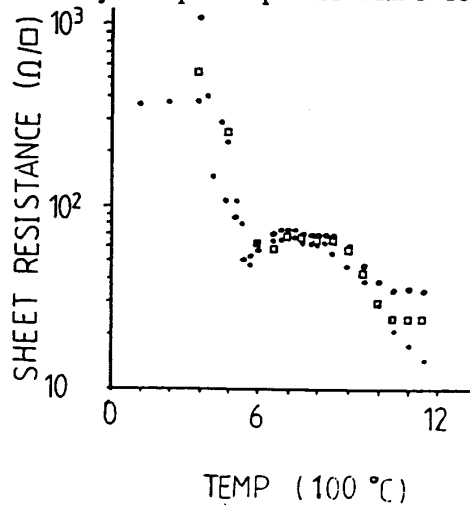


Figure 2 Dependence of sheet resistance upon isochronal anneal temperature for (•) bulk silicon, (○) SIMOX with 2000Å and (□) SIMOX with 3000Å of silicon overlay. All samples implanted with $5 \cdot 10^{15} \text{As cm}^{-2}$ at 70keV.