On the role of dislocation loops in silicon light emitting diodes

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The role of boron-induced dislocation loops on the suppression of the luminescence thermal quenching in silicon-based light-emitting diodes is investigated here. Luminescence measurements and cross-sectional transmission-electron-microscopy images from devices fabricated by boron implantation into crystalline silicon, and into a pre-amorphized substrate, to prevent the boron-induced loops formation, were compared. The results show that, in the devices incorporating dislocation loops between the depletion region and sample surface (the boron induced loops), the thermal quenching has been completely eliminated, in contrast with devices fabricated from the pre-amorphized substrate where strong thermal quenching is still observed. © 2005 American Institute of Physics. [DOI: 10.1063/1.2130533]

Silicon is an indirect band gap semiconductor and is fundamentally unable to emit light efficiently. Nevertheless, silicon is the basis of the microelectronic industry and several approaches have been considered to enhance its poor light emission. These include porous silicon, silicon-silicon dioxide superlattices, silicon nanoprecipitates in silicon dioxide, erbium in silicon, silicon/germanium, and iron disilicide. A common problem found in all these approaches is the strong thermal quenching leading to very poor performance at room temperature. Ng et al. proposed a unique solution, the dislocation engineering method, to suppress the thermal quenching in semiconductor systems. The basis of the dislocation engineering approach is the controlled introduction, in a p-n junction between the depletion region and the surface, of dislocation loops. The loops modify the silicon band gap, on the p-side, to provide spatial confinement of charge carriers. Thus, diffusion of injected carriers to point defects and the surface, where efficient nonradiative recombination occurs, is minimized or eliminated. This approach was used, for example, to fabricate β-FeSi2 light emitting devices (LEDs) and reduced the luminescence thermal quenching to only one order of magnitude of that of the low temperature emission, making these devices much more efficient than conventional ones, where room temperature luminescence was practically absent.

In this paper we present a detailed study of the role of the dislocation loops on the suppression of the luminescence thermal quenching. In order to directly compare the luminescence of devices with and without the incorporation of dislocation loops above the p-n junction, two batches of samples were designed. Devices were fabricated by boron implantation (1015 B cm−2 at 30 keV) into either a standard [100] n-type silicon substrate, phosphorous doped (2–7 Ω cm) or on an otherwise identical substrate but previously amorphized by a high energy silicon implantation (6 × 1015 Si cm−2 at 400 keV). Thus, boron induced dislocation loops were formed above the junction in the crystalline substrate, but not in the pre-amorphized material. In both cases, the boron implantation provided the p-type dopant to form the p-n junction. Subsequent to the boron implantation, all samples were annealed at 950 °C for 20 min in N2 ambient. Fabrication of light emitting devices, in both cases, was completed by deposition of front and back contacts and mesa etching, as described previously. Devices fabricated from the substrate containing the dislocation loops will be referred to as dislocation engineered light emitting diodes (DELEDs) and the ones fabricated from the pre-amorphized material will be referred to as conventional devices. The devices were characterized by electroluminescence (EL), photoluminescence (PL), and cross-sectional transmission electron microscopy (X-TEM). The EL and PL experiments were performed in the temperature range 80–300 K, as described in Ref. 9. PL measurements were done at 100 mW using the 488 nm line of an Ar laser. EL measurements were performed under forward bias (current density of 3 A/cm2) extracting the light through a window at the back of the samples. Current-voltage characteristics were measured prior to the EL experiments and, in both cases, showed good diode characteristics, with a turn-on voltage of less than 1 V. The basic procedure for the XTEM measurements is described in Ref. 10.

Cross-sectional TEM analysis after boron implantation and annealing is presented in Fig. 1. It shows that boron induced dislocation loops are present only in samples fabricated from the crystalline substrate; implantation of boron in pre-amorphized substrate does not induce the formation of dislocation loops. Figure 1(a) shows the X-TEM image of samples fabricated from the crystalline substrate. It can be seen that dislocation loops are located at a depth of ~140 nm, above the depletion region edge at ~350 nm. This is in agreement with the predicted ion range (1091 Å) and straggler (394 Å) for 30 keV B implantation into Si. These loops are extrinsic, either faulted or perfect, lying in {111} Si habit planes as described earlier. Figure 1(b) shows X-TEM image taken from samples fabricated from the pre-amorphized substrate. In this case only end of range (EOR) dislocation loops with respect to Si ion range are present. These are smaller loops and located much deeper in

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the substrate. They are also extrinsic in nature, faulted or perfect, situated at a depth of \( \sim 700 \) nm from the Si surface, in accordance to the theoretical range (6092 Å) and straggle (1458 Å) for 400 keV Si ions implanted into Si.\(^{11}\)

Figure 2 shows the room temperature photoluminescence spectra of samples with and without dislocation loops. Room temperature PL is observed only from the sample containing dislocation loops. Figure 3 shows the EL measured at 80 K and at 300 K for both types of devices. The EL response for the dislocation engineered LED is dominated, at low temperatures, by a strong, narrow line peaked at 1.1 \( \mu \)m, corresponding to the Si band-edge emission. Emission at 1.1 \( \mu \)m is practically absent in the conventional device at 80 K, its EL response being dominated by a broad line in the region 1.3–1.7 \( \mu \)m. At room temperature, EL at the silicon band edge is observed from both devices. However, the DELEDs are ten times brighter than the conventional devices. Figure 4 shows the EL integrated intensity plotted against measurement temperature for a DELED and a conventional device. The EL integrated intensity increases with temperature for the DELED, and decreases for the conventional device.

The increase of EL integrated intensity with temperature observed only in the dislocation engineered light emitting diodes clearly shows that the incorporation of boron induced dislocation loops between the device active region and surface is responsible for the elimination of the luminescence thermal quenching. The dislocation loop array introduces a strain field outside the loop that increases the silicon band gap by up to 0.75 eV\(^{7}\) and enables spatial confinement of the injected carries, so carrier diffusion to point defects and the surface, where efficient non-radiative recombination occurs, is suppressed. Dislocation loops above the junction were not formed by boron implantation into the pre-amorphized substrate. Indeed, only Si induced end-of-range dislocations, located much deeper in the substrate, could be observed, so
spatial confinement of charge carriers to prevent nonradiative recombination was absent. These end of range dislocations are probably the origin of the EL emissions observed at 80 K. These lines, at 0.82, 0.86, and 0.92 eV, are the D1, D2, and D3 lines, commonly reported to appear in ion implanted silicon.\textsuperscript{12} They are also thought to be related to the small increment in the silicon integrated intensity with temperature observed in the pre-amorphized devices. In this situation, the silicon emission is fed from the emission from the D lines. Indeed, an increase in silicon band edge emission due to a simultaneous decrease in other centers has been observed and successfully modelled before.\textsuperscript{8}

The external quantum efficiencies collected, just through the windows, of the devices into a hemisphere are 0.02\% for the dislocation engineered LED and 0.002\% for the conventional silicon LED. It should be noted that, because these are planar devices, the measured total external quantum efficiency is limited by total internal reflection that reduces the extraction efficiency through the front surface by a factor of ~60 of the total EL produced internally. This effect is simply the result of geometric optics and could be largely eliminated by commercial LED packaging or by surface texturing, as in the case of the silicon diode reported by Green et al.,\textsuperscript{13} where an external quantum efficiency of ~1\% in high purity silicon devices was reported. In general, the quantum efficiency in a LED is a function of injection current\textsuperscript{14} because competing nonradiative routes, in particular Schockley-Hall-Reed (SHR) recombination, through bulk and surface defects at low injection currents, and Auger recombination at very high injection. The EL of the dislocation engineered devices reported here is found to be linear up to the maximum current densities measured of 15 A/cm\textsuperscript{2}. This is consistent for a low to medium injection regime where, as postulated here, recombination through defects (the SHR route) have been decoupled from the radiative route and before the onset of significant Auger recombination.

In summary, we have demonstrated here that the controlled introduction of dislocation loops, to allow their location, mean size and density to be engineered, is responsible for the suppression of the luminescence thermal quenching previously observed in silicon-based light emitting diodes. Dislocation engineered light emitting diodes are efficient at room temperature and generally brighter than at lower temperatures. In contrast, thermal quenching is strong in devices fabricated without the introduction of dislocation loops into the active region of the device. It is the stress field introduced by the array of dislocation loops that creates a blocking potential that, when placed adjacent to and just above the depletion region edge, prevents carrier diffusion to point defects and surface, where efficient nonradiative recombination occurs, so radiative recombination is enhanced. Thus, by decoupling the nonradiative recombination routes, the luminescence thermal quenching can be suppressed or eliminated, as band-to-band transitions are relatively temperature independent, in contrast to nonradiative routes which show strong temperature dependence.