Boron pile-up phenomena during ultra shallow junction formation

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The redistribution during annealing of low-energy B implants in SOI structures and in bulk Si have been investigated by comparing Secondary Ion Mass Spectrometry (SIMS) and simulated profiles. Samples preamorphised with Ge at different implantation energies have been prepared in order to investigate the effects of the damage position on B diffusion. The specimens have been subsequently B implanted at 500 eV with doses 2x10^{13} and 2x10^{14} cm^{-2} and annealed between 700 and 1100 °C. SIMS profiles show a B pile-up in the first few nanometres of the Si matrix on the Si surface. Simulations of diffused profiles indicate that the B redistribution upon annealing can be explained by assuming that the mobility of the dopant which arrives in proximity of the surface is practically annulled. The amount of B trapped at the surface is maximum at the temperatures around 800 °C, when more than 80 % of the implanted dopant is made immobile and electrically inactive. The trapped B increases with reducing the depth of the amorphous layer and it is higher in the bulk Si than in SOI. By comparing Hall measurements and the amount of B not trapped at the surface, we also estimate the amount of B that aggregates inside the Si lattice in form of clusters (BICs). For the B dose of 2x10^{14} cm^{-2}, after isochronal annealing of 60 s, the amount of BICs is about 3-4x10^{13} cm^{-2} at the lowest temperatures and tends to vanish at high temperatures.

Introduction

Scaling of electronic devices requires both the reduction of all the physical dimensions, including junction depth profile, and high levels of active dopant concentration. Highly active, shallow and steep doping profile can be obtained by using preamorphising implants prior dopant implantation and annealing at low temperature exploiting the solid phase epitaxial re-growth (SPER) process [1]. It is well known that, as a consequence of the amorphising implant, an excess of Si interstitials forms just beyond the amorphous/crystalline interface in the end-of-range (EOR) region. During subsequent annealing these defects evolve through the formation/dissolution of aggregates and the release of free self interstitials [1-4]. This interstitial supersaturation causes unwanted phenomena like transient enhanced diffusion.
point defects clusters and clustering between point defects and dopant with consequent electrical deactivation [1,2]. The surface (and the interfaces) affect the impurity profile both through the segregation and trapping of dopant atoms in energetically favourite places and acting on point defects distribution, and consequently, on the TED phenomena. These effects are particularly evident for boron [5-8] and phosphorous [9, 10], but have been also evidenced in ultrashallow junctions obtained by low energy arsenic implantation [11, 12].

In these last years the interest for silicon-on-insulator (SOI) technology has strongly increased due to the expected superior performances and fabrication advantages of this material with respect to the bulk silicon [13]. As far as the diffusion phenomenon is concerned, the presence in the SOI samples of a buried Si/SiO2 interface provides another efficient sink for the interstitials besides the silicon surface. This second sink modifies the distribution of the point defects during the annealing and consequently the unwanted effects related to these defects [14].

Purpose of this work is to investigate the uphill diffusion in ultrashallow p+/n junctions obtained by low energy B implantation in both bulk Si and SOI material. In particular, the dependence of this phenomenon on implanted dose, depth of the amorphous layer and annealing conditions has been analyzed with the support of a simulation program which takes into account the dopant segregation at the surface and allows a satisfactory describe of the dopant redistribution upon annealing.

**Experimental**

The experiments were performed on (100) oriented, n-type CZ silicon wafers with resistivity 10-25 Ω.cm, and on SOI wafers with a 55 nm-thick p-type Si over layer obtained by wafer bonding technology. The samples were Ge implanted to produce amorphous layers of different depth. With a Ge dose of 1x10^{15} cm^{-2} and energies of 8, 20 and 25 keV we amorphised the silicon to depths of about, 20, 40 and 45 nm, respectively [15]. The specimens were subsequently B implanted at 500 eV to doses of 2x10^{13} and 2x10^{14} cm^{-2}. The annealing treatment were performed at temperatures between 700 and 1100 °C for different times between 5 s and 4 h using a Process Products Corporation 18 Lamp rapid thermal processing (RTP) annealer in nitrogen atmosphere. To monitor the temperature in the annealer, we use a k-type thermocouple attached to the bottom of a 5” Si support wafer and the samples to be annealed are placed on top of this support wafer. From the comparison of the re-growth rate of the amorphous layer we found that the bulk Si samples experienced about 3 °C higher temperature than the SOI specimens. This is within the uncertainties of the techniques used.

CAMECA Sc-Ultra mass spectrometer has been used to perform Secondary Ion Mass
Spectroscopy (SIMS) depth profiles. An oblique incidence (68°) 500 eV O²⁺ primary beam has been used and positive secondary ions were collected. In order to obtain a suitable depth resolution and sensitivity a controlled oxygen leak was introduced in the analysis chamber and sample underwent a rotation during sputtering [16].

The electrical activity of the B implants was assessed by sheet resistance and Hall measurements using an Accent HL5500 machine.

The simulations of the diffused profiles are performed by using a pair-diffusion model which considers unpaired point defects and dopant-defect pairs as mobile species and the unpaired dopant on lattice sites as immobile species. To take into account the B uphill diffusion toward the surface we introduce a “trap” distribution into the Si in the first 2-3 nanometres below the Si/gas interface. All the simulations are performed with an equilibrium constant reaction which favours a rapid formation of the B-Trap complexes and with the trap density used as fitting parameter. This simple approach allows us to strongly improve the agreements between simulation and measured profiles in a wide range of experimental conditions. A more detailed description of the model can be found in Ref. [17].

Results and discussion

Fig. 1 shows the SIMS profiles of Ge PAI samples at 20 keV and B implanted with dose $2 \times 10^{13}$ cm$^{-2}$ after annealing for 60 s at various temperatures both in SOI (Fig. 1a) and in bulk material (Fig. 1b). In dotted line the simulated profiles are also reported. SIMS profiles clearly show a B pile-up in the first few nanometres of the Si matrix on the Si surface. The simulations permit to describe the phenomenon of the uphill diffusion and to evaluate the amount of B collected on the surface region.

Fig. 1: Comparison between simulated (dashed lines) and SIMS (continuous lines) profiles of PAI samples at 20 keV, B implanted with dose $2 \times 10^{13}$ cm$^{-2}$ after isochronal annealing of 60 s at various temperatures in SOI (Fig. 1a) and in bulk material (Fig. 1b).
Simulations indicate that the B diffusion, apparently against the concentration gradient, is consistent with Fick’s diffusion equation if the diffusivity near the surface suddenly drops to negligible values; this effect is performed in the simulation by assuming the presence of traps in that region without considering any asymmetric behaviour of the dopant movement. The presence of unsaturated traps in the surface region drives the [BI] pair concentration to be very small, and thus creates a huge gradient between the B profile and the traps region. This gradient drives the phenomenon of the uphill diffusion. On the contrary, simulations performed without the introduction of traps near the surface bring to a relevant overestimation of the junction depth and an incorrect determination of the B concentration in the surface region (not shown).

By using the SIMS profiles and simulations the amount of B which is distributed into the Si, and not trapped at the surface, can be evaluated. The obtained results are reported in Fig. 2 for the B implanted dose of $2 \times 10^{13}$ cm$^{-2}$ as a function of the annealing temperature for a 60 s treatment. The data refer to the bulk samples preamorphised with Ge implantation at 8 and 20 KeV. In all samples there is a minimum in the free-B amount around 800-850 °C; at these temperatures more than 80 % of the implanted B is trapped at the surface.

Boron segregation at the surface depends on annealing temperature. At low temperatures (700-775°C) the low B diffusivity limits the dopant atoms to reach the surface then the segregation is reduced. This is confirmed by the dependence of the trapped B amount on the position of the EOR defects. Higher trapping is obtained in the samples where the dissolving EOR defects, which release interstitials and promote B diffusion, are nearer to the surface. At high annealing temperature, B segregated at the surface reduces.

![Fig. 2: Amount of not trapped B vs annealing temperature for isochronal treatments of 60 s in bulk samples PAI with Ge at different energies, in order to locate the EOR defects at different depths. The B implanted dose was $2 \times 10^{13}$ cm$^{-2}$. This is due both to the lowering of interstitial supersaturation with increasing temperature that allows for B atoms to avoid the surface segregation diffusing inside the bulk and to the tendency of segregation phenomena to reduce at higher temperature. However, only for temperature as higher than 1050 °C we have a nearly complete release of the trapped B (see Fig. 2).

The different evolution of the interstitial supersaturation is also responsible for the differences of B trapping between SOI and bulk.
Si shown in Fig. 3a and Fig. 4 for samples B implanted with dose $2 \times 10^{14}$ cm$^{-2}$.

In fact, in SOI samples the buried oxide acts as a sink for interstitials increasing EOR dissolution rate and reducing the enhancement of the B diffusion [7]. The effect is more evident for the samples preamorphised at 20 keV due to the proximity of the EOR defects to the buried oxide layer. Also for this dose the surface trapping in the samples preamorphised at 8 keV is higher respect to the one exhibited by the samples more deeply preamorphised at 20 keV.

The trend of the not trapped B vs temperature shown in Fig. 3a is in qualitative agreement with the one of the active B concentration reported in Fig 3b. Two effects contribute to the B deactivation: i) B segregation in proximity of the surface and ii) formation of Boron interstitial clusters (BICs) inside the Si lattice in presence of high interstitial supersaturation [18]; both these contributions increase with increasing interstitial supersaturation. However, the good correlation between hole concentration and not trapped B indicates that surface segregation is the dominant effect to determine the amount of active B in these experimental conditions.

Fig. 5 shows the comparison between the hole concentration and the not trapped boron of Ge PAI samples at 8 keV and B implanted with dose $2 \times 10^{14}$ cm$^{-2}$ after annealing for 60 s at various temperatures in the SOI material. The difference between the two curves, i.e. the amount of BICs

**Fig. 3:** Amount of not trapped B (3a) and hole concentration (3b) vs annealing temperature for isochronal treatments of 60 s in SOI samples and bulk material PAI with Ge at different energies. The B implanted dose was $2 \times 10^{14}$ cm$^{-2}$.

**Fig. 4:** Comparison of the B profiles of samples implanted with dose $2 \times 10^{14}$ cm$^{-2}$ and annealed at 800 $^\circ$C for 60 s in SOI and bulk Si PAI with Ge at different energies.
present after 1 min annealing, is in the range 3-4x10^{13} \text{ cm}^{-2} at low temperatures and then reduces with increasing temperature down to vanish, in agreement with the dissolution (or not formation) of these clusters (see Fig. 5). A value of 3-4x10^{13} \text{ cm}^{-2} means that at low temperature about 30-40\% of the B located inside the silicon is in form of BICs.

Conclusions

Ultrashallow junctions obtained by low energy (500 eV) B implanted in preamorphised SOI and in bulk Si show, after annealing, a strong effect of uphill diffusion which confines, and makes electrically inactive, a relevant amount of dopant in proximity of the surface. This redistribution can be accounted for by simulations based on pair diffusion model and with the introduction of trapping sites in proximity of the surface, without considering any asymmetric behaviour of the dopant movement. The amount of trapped B increases with decreasing temperature, and, for a fixed temperature, is higher in the conditions of higher diffusivity. The correlation between trapped B and diffusivity allows explaining the

Fig. 5: Comparison between not trapped B and hole concentration vs annealing temperature for isochronal treatment of 60 s in SOI sample PAI with Ge at 8 keV and B implanted at 2x10^{14} \text{ cm}^{-2}.

The junction depths, X_j, evaluated from SIMS profiles at the concentration of 1x10^{18} \text{ cm}^{-3}, for the samples B implanted with dose 2x10^{14} \text{ cm}^{-2} are reported in Fig. 6 as a function of the annealing temperature. For temperatures higher than 850 °C a shallower X_j in SOI material with respect to bulk Si is obtained in agreement with previous observations [19]. This effect was attributed to the interstitial recombination on the buried oxide layer [20, 19].

Fig. 6: Junction depths (X_j), evaluated from SIMS profiles at the concentration of 1x10^{18} \text{ cm}^{-3}, for the samples B implanted with dose 2x10^{14} \text{ cm}^{-2} as a function of the annealing temperature for isothermal treatments of 60 s. The figure shows the results of SOI and bulk Si PAI with Ge at 8 and 20 keV.
increase of the trapped B with reducing the depth of the amorphous layer and the differences observed between SOI layers and bulk Si. At the temperatures around 800 °C, more than 80% of the implanted dopant is trapped in the surface region.

By comparing Hall measurements and the amount of B not trapped at the surface, we estimate the amount of BICs formed during the annealing. For the B dose of 2×10^{14} cm^{-2}, after isochronal annealing of 60 s, the amount of BICs is about 3-4×10^{13} cm^{-2} at the lowest temperatures and tends to vanish at high temperatures.

Acknowledgments

This work was partially supported by the FIRB project (contract RBNE012N3X).

Bibliography

